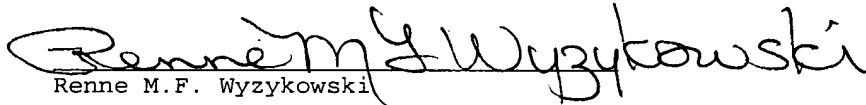




2133  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on April 28, 2004.

  
Renne M.F. Wyzykowski

Appl No. : 09/851,708 Confirmation No. 3552  
Applicant : Indradeep Ghosh  
Filed : May 8, 2001  
Title : AUTOMATIC TEST PATTERN GENERATION FOR FUNCTIONAL  
REGISTER TRANSFER LEVEL CIRCUITS USING ASSIGNMENT  
DECISION DIAGRAMS

TC/A.U. : 2133  
Examiner : R. Stephen Dildine, Jr.

Docket No. : 40704/DMC/F179  
Customer No. : 23363

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MAY 05 2004

Technology Center 2100

AMENDMENT

Mail Stop NON-FEE AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Post Office Box 7068  
Pasadena, CA 91109-7068  
April 28, 2004

Commissioner:

In response to the Office action of January 30, 2004,  
please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 4 of this paper.